



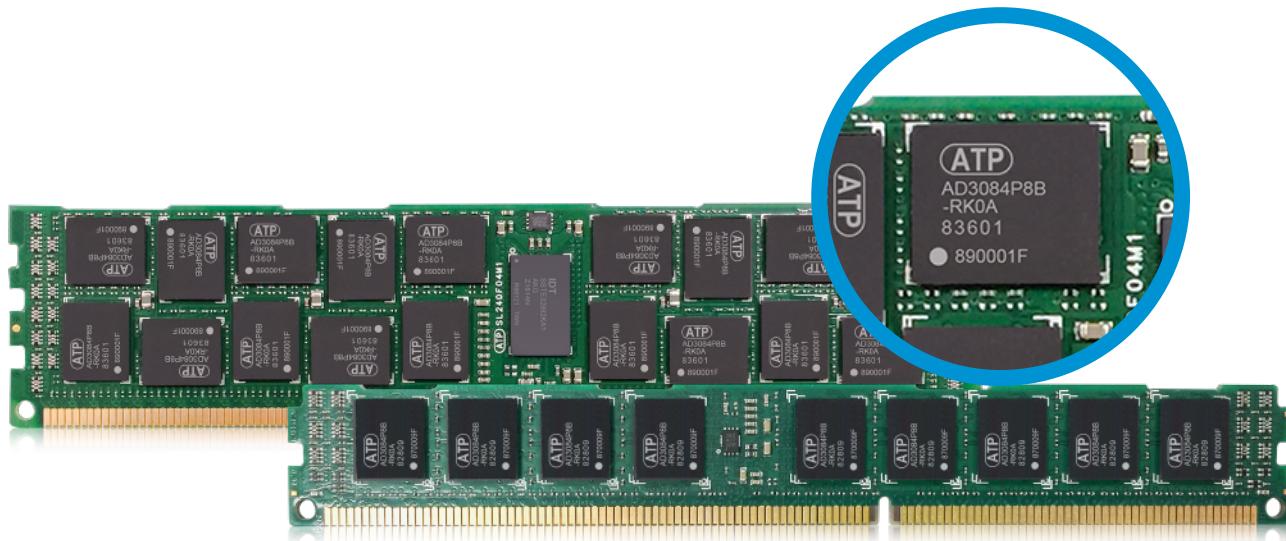
ATP Reaffirms Commitment to Long-Term DDR3 Legacy Memory Module Supply with ATP DDR3 8 Gbit components

As the DRAM market makes a steady migration to DDR4 memory, several key manufacturers have already announced end-of-life (EOL) production of DDR3 modules based on high density DDR3 8 Gbit components including EOL notice of the components. However, a sizable number of customers in the networking and embedded industries are still unable to shift to the latest generation and continue to use legacy systems requiring specific DDR3 memory such as high-density SO-DIMMs. To avert a supply shortage that could adversely affect these customers' business operations, ATP has decided to provide its own DDR3 8 Gbit components for these modules.

ATP-Built from IC to Module

ATP's own-built DDR3 modules consist of meticulously characterized and tested high-quality integrated circuits (ICs). The components are manufactured according to ATP's exacting standards using 2x nm manufacturing process technology and are tested via an extensive component test program to improve the overall memory module performance.

ATP DDR3 8 Gbit components are free from row hammer effects, thus preventing any disastrous random bit flips caused by the electrical charge of cells leaking to adjacent cells and successively writing data to them. At module level, ATP implements 100% test during burn-in (TDBI) into the production flow to guarantee the high quality module.



ATP DDR3 Configurations

A typical monolithic DDR3 DRAM chip has a density of 4 Gigabits (Gb). To pack 8 Gb in a monolithic DRAM die, manufacturers employ a die-stacking method called dual-die package (DDP), which combines two bare memory dies within a single chip package. Each die has a separate set of control lines where each memory die is separately selectable, and the processor treats the chip as two components despite being in the same package.

ATP DDR3 components are available in monolithic 8 Gb one-chip select (1CS) or as DDP two-chip select (2CS) for a variety of memory modules based on this technology.

With ATP's own-built DDR3 modules, the company reaffirms its commitment to continue supporting legacy memory requirements to maximize customers' infrastructure investments.

DDR3 8Gbit Component					
Form Factor	ECC RDIMM	UDIMM	UDIMM ECC	SO-DIMM	SO-DIMM ECC
Density	16 GB				
Ranks	2				
Component Org	1G x 8				
Component Qty.	18	16	18	16	18
Speed up to (MT/s)	1600				
Technology	Mono				



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Product spec and its related information are subject to change without advance notice.

Please refer to www.atpinc.com for latest information

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